

FIG. 1

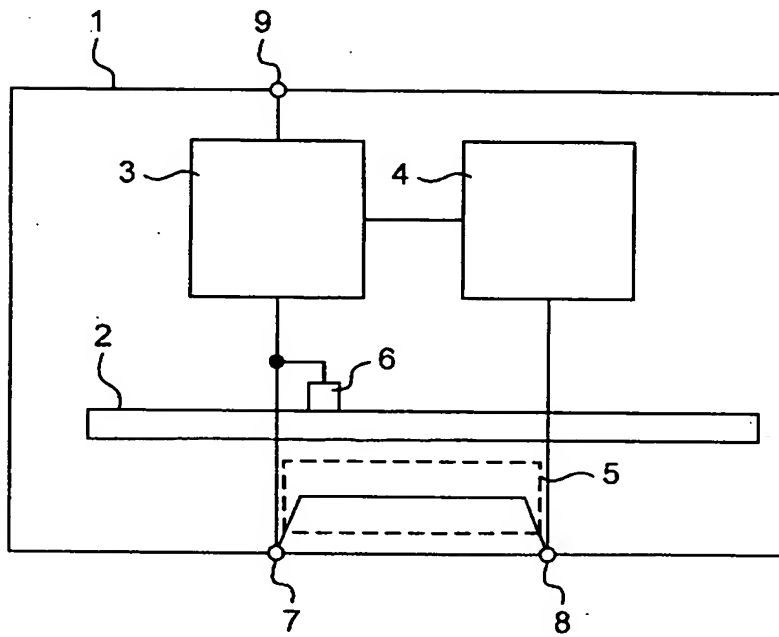


FIG. 2

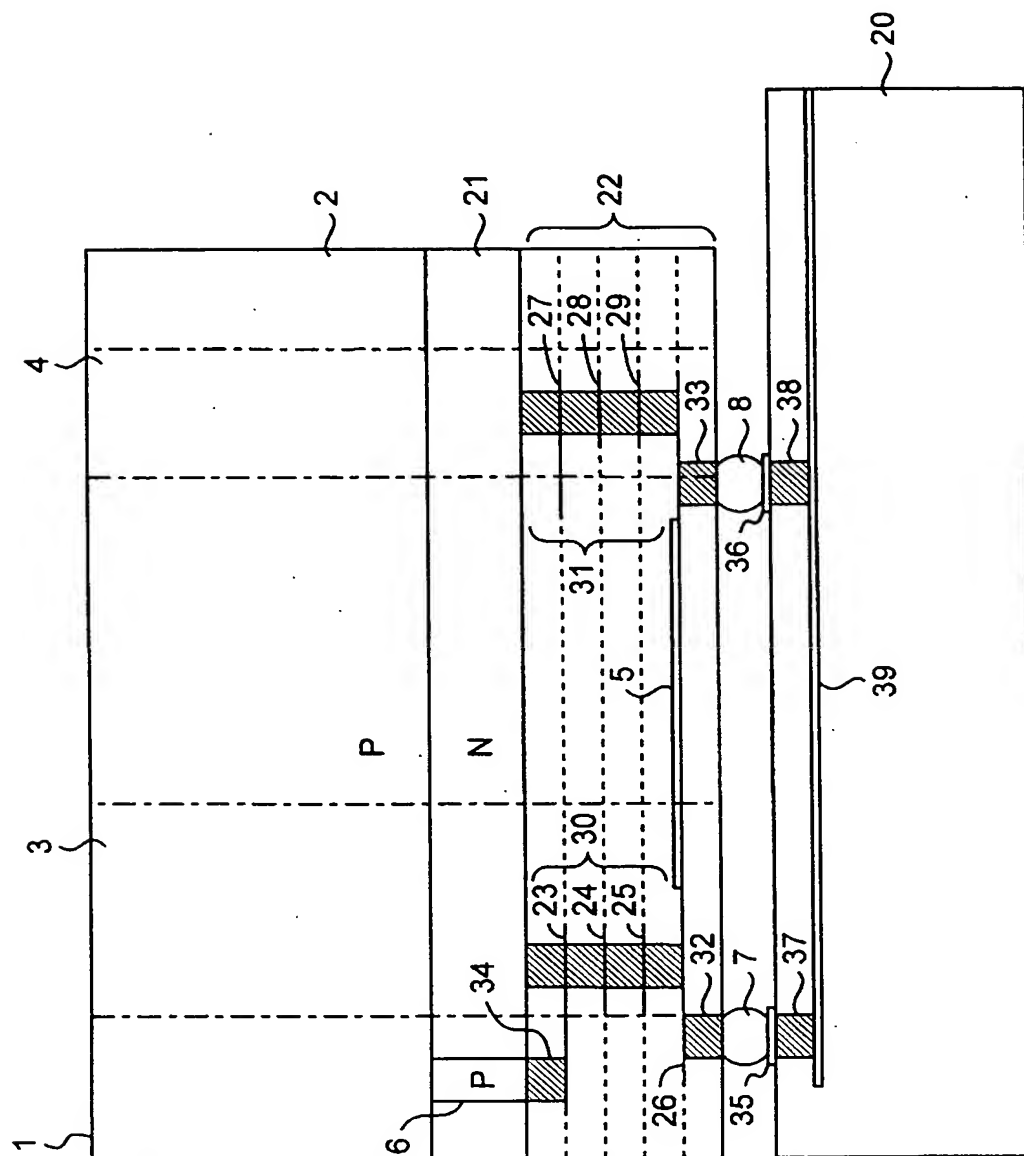
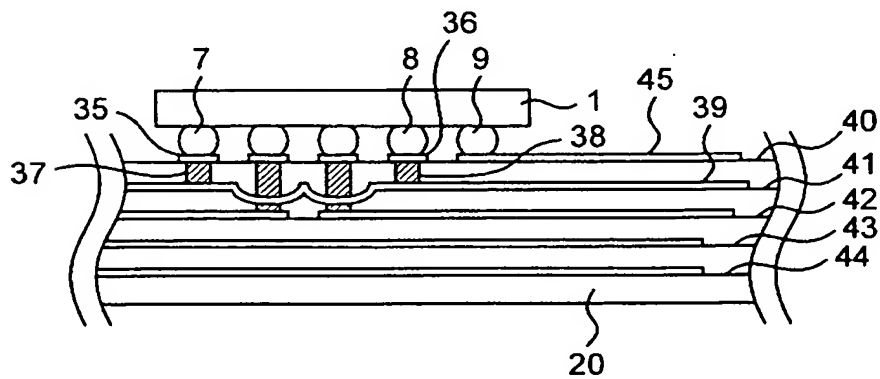


FIG. 3



This diagram shows a second embodiment of the electrode array. It features a horizontal top rail (1) connected to ground at both ends. Five circular electrodes (5) are arranged in a single row along the bottom of the top rail. A dashed line indicates a connection path from the leftmost electrode (5) to the rightmost electrode (5) through the intermediate electrodes. Labels 7, 8, and 9 point to the first, second, and third electrodes from the left, respectively.

The diagram shows a transmission line model. An input voltage source V_i is connected to a series inductor L_{52} . After L_{52} , there is a node labeled '7'. A shunt inductor L_{54} is connected from this node to ground. The line continues through inductor L_{56} to another node. A shunt inductor L_{55} is connected from this node to ground. The line then passes through inductor L_{53} to a node labeled 'B'. A load impedance Z_L is connected from node 'B' to ground. The output voltage V_o is measured across Z_L . Points 'A' and '8' are also marked on the series line. Ground connections are labeled '51'.

FIG. 7

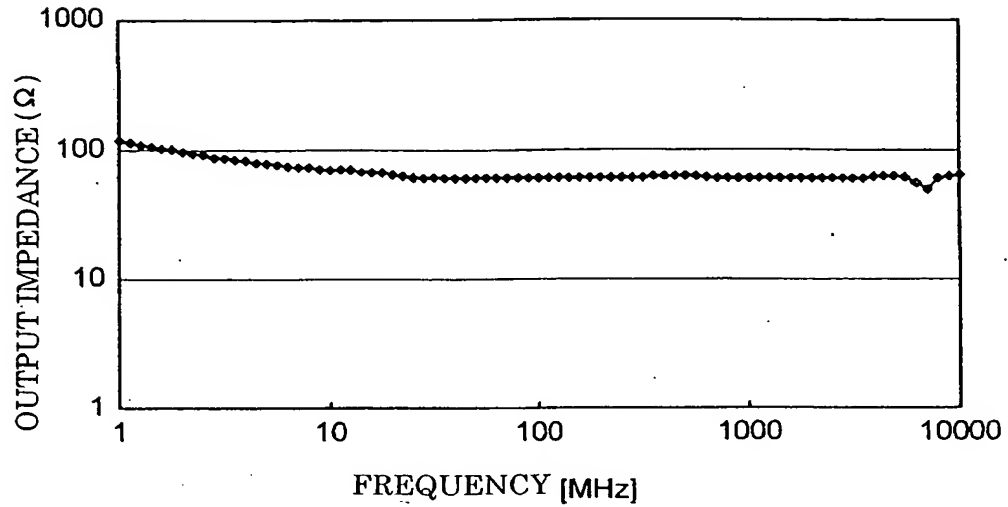


FIG. 8

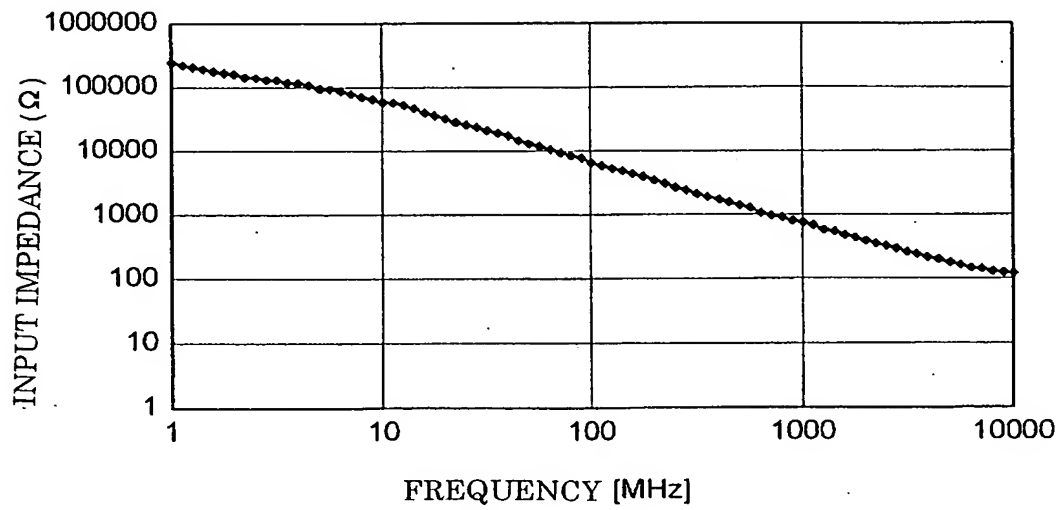


FIG. 9

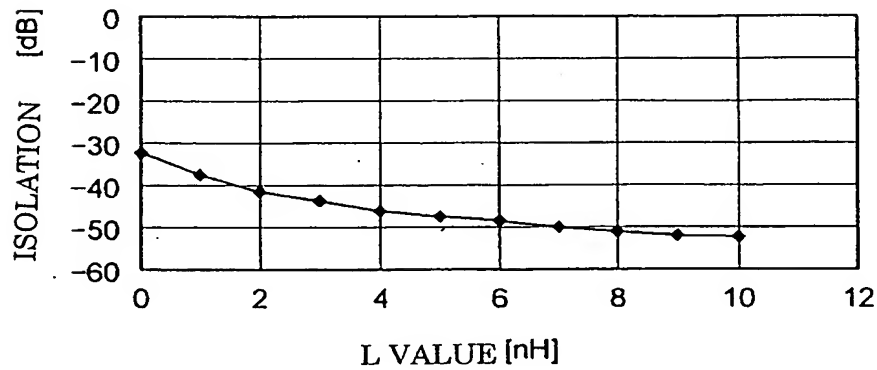


FIG. 10

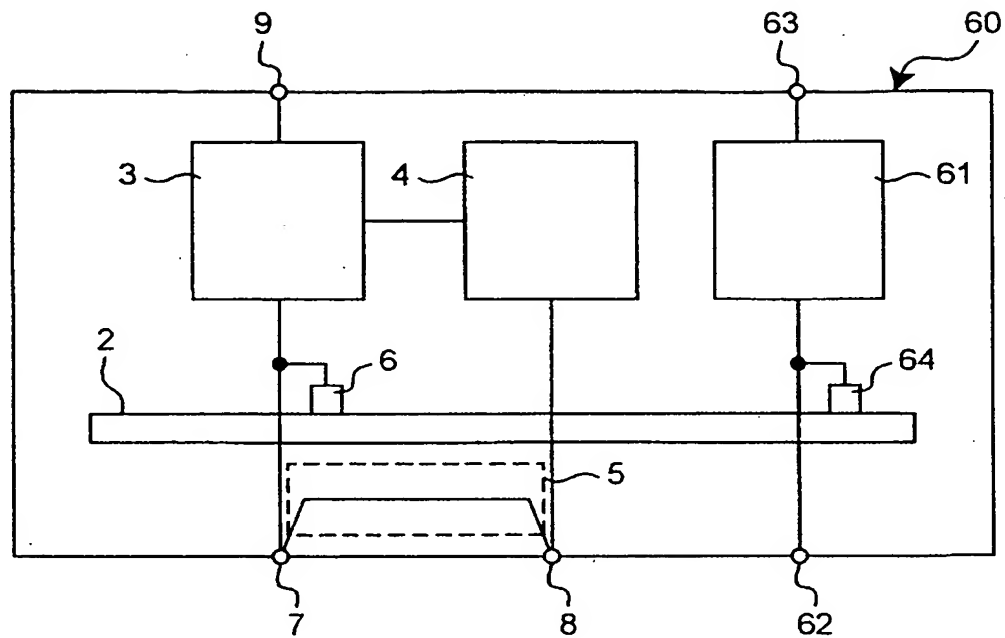


FIG. 12

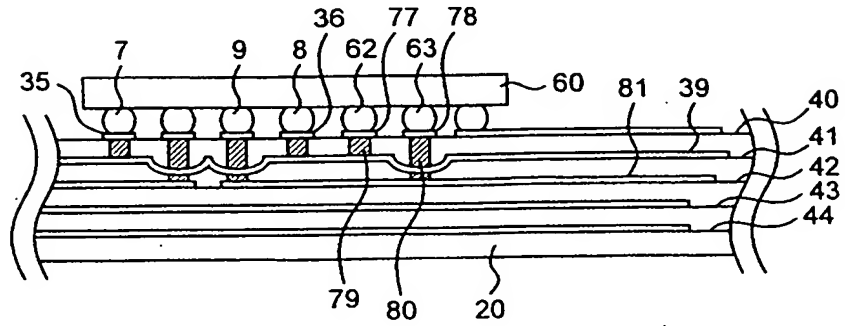


FIG. 13

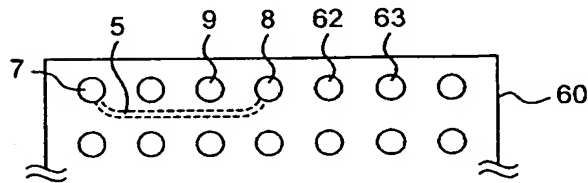


FIG. 14

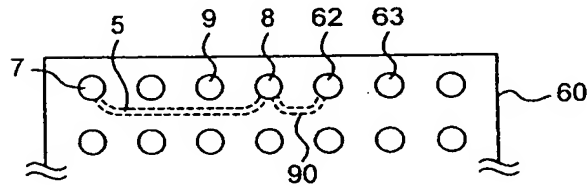


FIG. 15

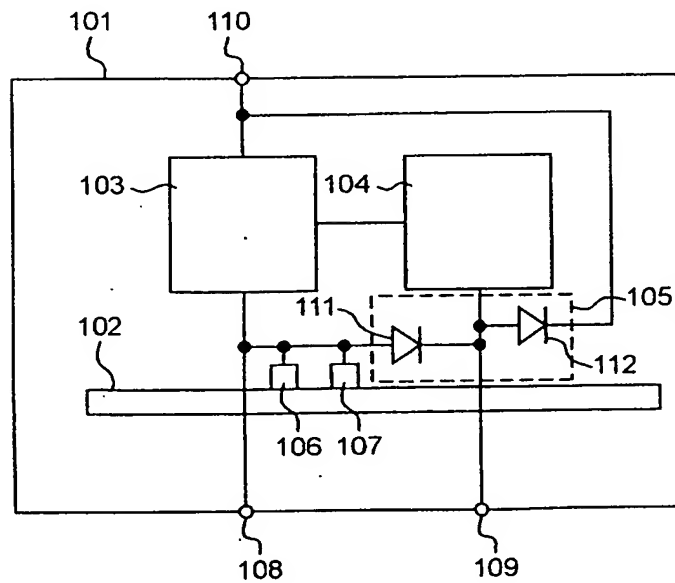


FIG. 16

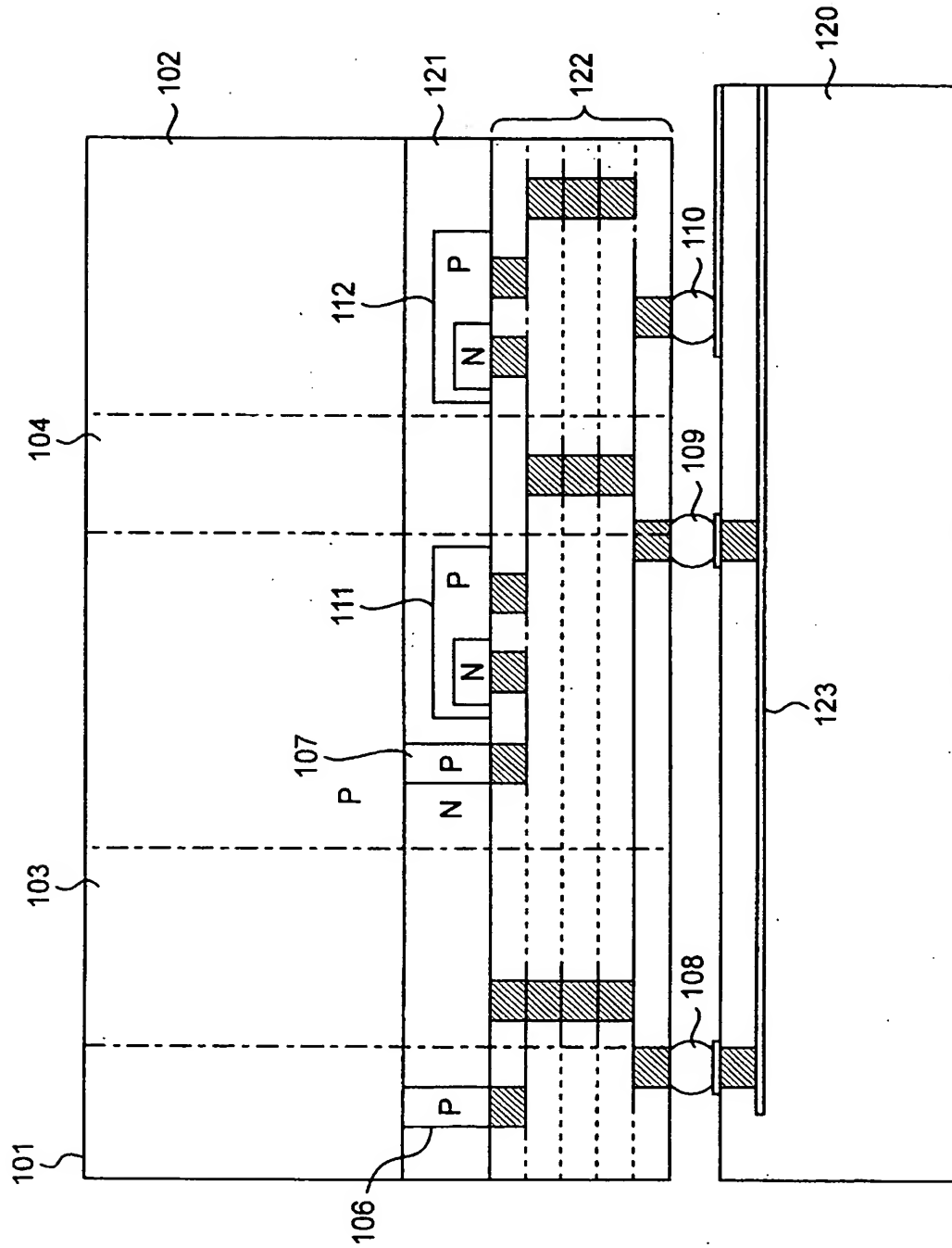


FIG. 17

